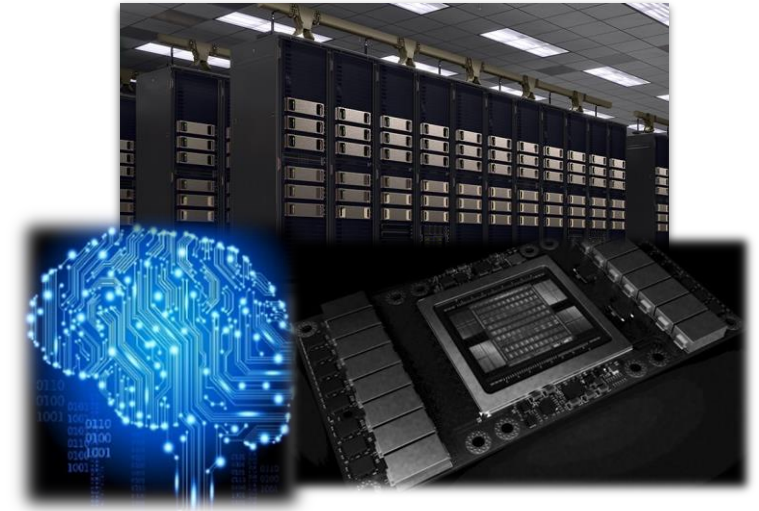


- Processor architectures  
(CPUs, GPUs, and hardware accelerators)
- Neural network acceleration systems  
(Deep learning accelerators, distributed learning systems)
- Memory system  
(Terabyte-scale memory, NVM, virtual memory)
- Processing-in-memory
- Hardware/software co-design for energy-efficient datacenters
- Parallel programming (for CPUs and GPUs)



**Research on  
Next-generation  
Computing systems**

**Please contact Prof. Yunho Oh ([yunho.oh@skku.edu](mailto:yunho.oh@skku.edu))**